



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: TANAKA et al

Serial No.: 10/648,231

Filed: August 27, 2003

For: System And Method For Evaluating A Semiconductor Device  
Pattern, Method For Controlling Process Of Forming A  
Semiconductor Device Pattern And Method For Monitoring  
A Semiconductor Device Manufacturing Process

Art Unit: 2825

Examiner: T. To

PETITION FOR AN EXTENSION OF TIME

Mail Stop: Amendment (Fee)  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 20, 2006

Sir:

In the matter of the above-identified application, applicants hereby respectfully petition for an extension of time to permit the filing of a response within the third month subsequent to the expiration of the shortened statutory period set in the outstanding Office Action mailed May 19, 2006.

A payment in the amount of \$1,020.00 to cover the fee required for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the deposit account of Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case: 520.43079X00).

Respectfully submitted,

ANTONELLI, TERRY, STOUT &amp; KRAUS, LLP

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